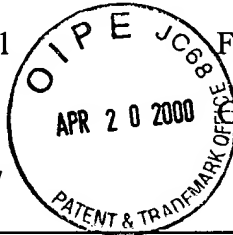


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PATENT
3 IDS
Lewis
4/27/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): James Casto et al.
Title: INTEGRATED CIRCUIT PACKAGE INCORPORATING
PROGRAMMABLE ELEMENTS
Application No.: 09/484,311 Filed: January 18, 2000
Examiner: Unknown Group Art Unit: Unknown
Atty. Docket No.: 1001-0087



April 14, 2000

ASSISTANT COMMISSIONER FOR PATENTS
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned wishes to bring the documents identified in the attached:

- ☒ PTO Form(s) 1449
- ☐ List of Pending U.S. Patent Applications
- ☐ Other:

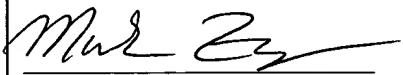
to the Examiner's attention in the above-identified application. Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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This Information Disclosure Statement is being filed:

- ☒ Prior to the first Office action on the merits or within three months from the filing date;
- ☐ After the mailing date of a first action on the merits, but before the mailing date of either a final action under § 1.113, or a notice of allowance under § 1.311, and is accompanied by the fee set forth in 1.17(p), which is being provided in the Transmittal Sheet enclosed herewith;
- ☐ Other:

CERTIFICATE OF MAILING	
I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.	
 Mark Zagorin	<u>4/14/00</u> Date

Respectfully submitted,



Mark Zagorin, Reg. No. 36,067
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